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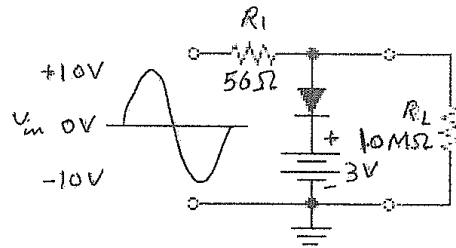
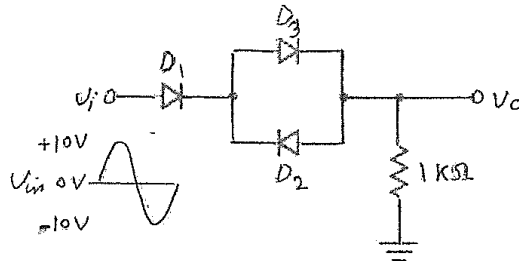
Kaferelsheikh University
Faculty of Engineering
Electrical Engineering Dept.
Final Exam, 2016-2017
Date: 2/1/2017



Subject: Electronic
Time : 90 minuets
Full Mark: 45 degree
Year: First
Exam in 2 page

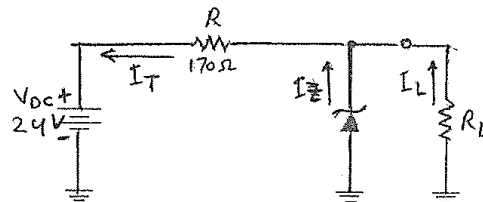
[1] Question One: (18 Mark)

- A) Distinguish between majority and minority carriers in a semiconductor. Define mobility of charge carriers.
- B) Discuss how a depletion layer is formed in a P-N diode and how does it vary with biasing ? Draw V-I characteristics of P-N junction diode.
- C) Derive an expression for the conductivity of a semiconductor.
- D) Determine v_o (R_L voltage) for each network of shown below for the same input shown.



[2] Question Two: (15 Mark)

- A) sketch diagram of dc power supply (with reference to the function of each component within the diagram).
- B) explain, full wave rectification using two diode? what is the difference between this method and using bridge?
- C) Determine the minimum and the maximum load currents for which the zener diode as shown in Fig. will maintain regulation. What is the minimum value of R_L that can be used? $V_Z=12V$, $I_{ZK}=1mA$, and $I_{ZM}=50mA$. Assume an ideal zener diode where $Z_z=0\Omega$ and V_z remains a constant 12 V over the range of current values, for simplicity. Explain if R_L is less than 490Ω .



تابع باقى الاسئلة فى خلف الورقة

with best wishes

Dr. noha abd al salam

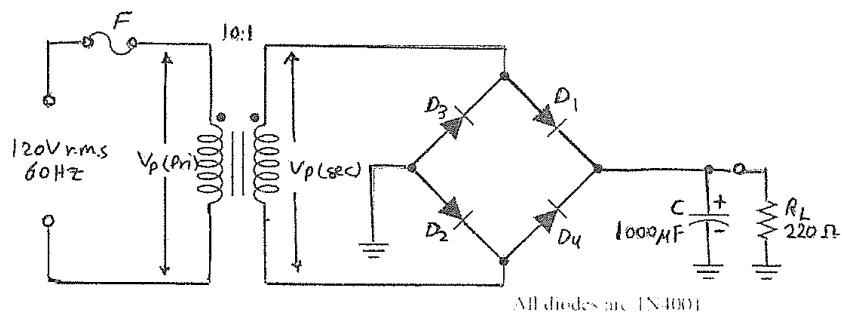
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[3] Question Three: (12 Mark)

A) source injects charge carriers into semiconductor bar. explain how current flow, what is this current?

B) compare between dynamic resistance and static resistance.

C) Determine the peak output voltage , what PIV rating is required for the diodes and the ripple factor for the filtered bridge rectifier with a load as indicated in Figure



with best wishes

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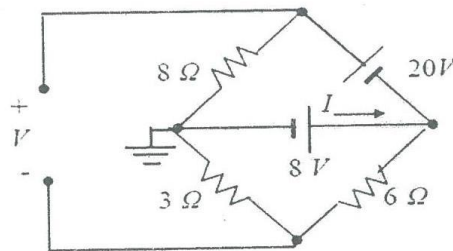


Answer the Following Questions:

Q1

For the circuit shown :

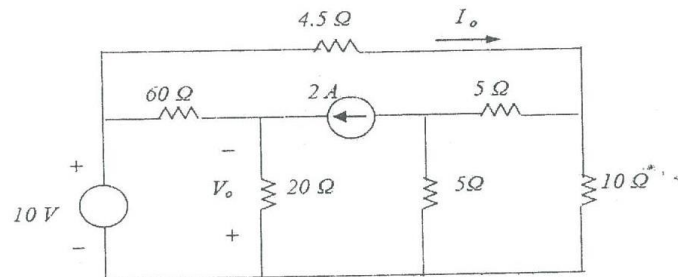
- Determine the current I.
- Find voltage V.



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Q2

Using the principle of superposition to find I_0 and V_0



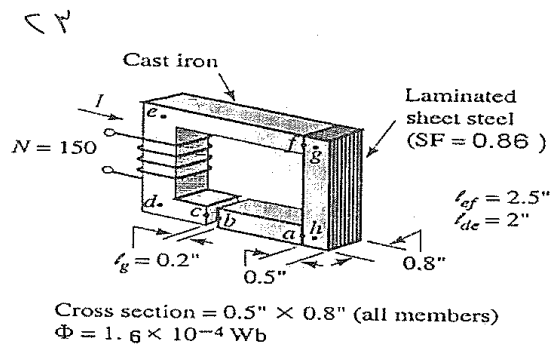
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Q3

- By drawing define the phase relation of between (which leading and by angle?):
 $v(t) = 40 \sin(\omega t + 50^\circ)$ and $v(t) = 20 \cos(\omega t - 30^\circ)$

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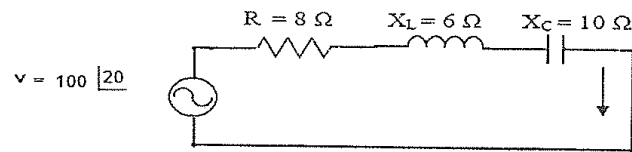
- The laminated sheet steel section in the figure has a stacking factor 0.86. Compute the current required to establish a flux $\phi = 1.6 \times 10^{-4}$ Wb. Neglect fringing. (Take $l'' = 2.54$ cm)



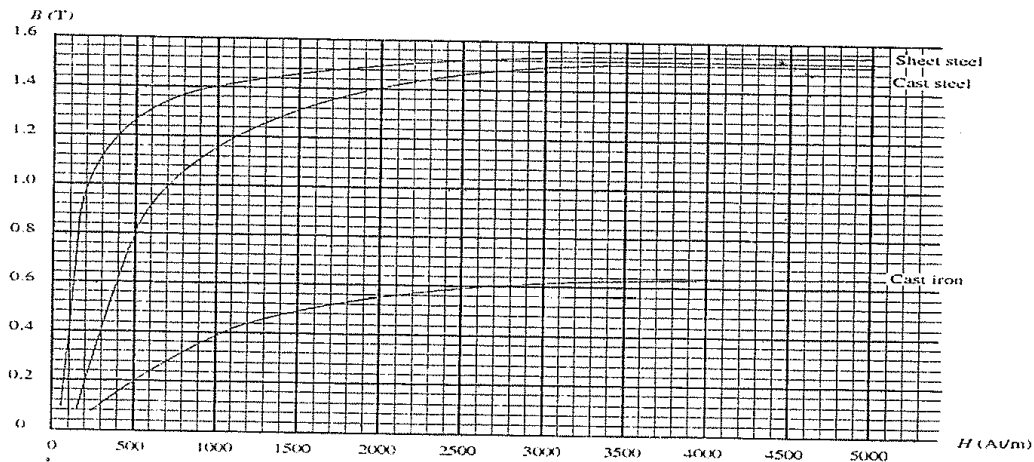
Q4

For the circuit shown below with $f = 50 \text{ Hz}$:

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- Find the total impedance Z in polar form.
- Draw the impedance diagram.
- Find the value of C in μF and the value of L in henry.
- Find the current I and the voltages V_R , V_L and V_C in polar form.
- Draw the phasor diagram of voltages V_R , V_L and V_C and the current I .
- Verify Kirchhoff's voltage law around the closed loop.
- Find the average power delivered to the circuit.
- Find the power factor of the circuit and indicate whether it is leading or lagging.
- Find the sinusoidal expressions for the voltages and current.
- Plot the waveforms for the voltages and current on the same set of axes.



End of Exam Questions, Good Luck
 Dr. fathalla selim
 Electronic part in other papers