







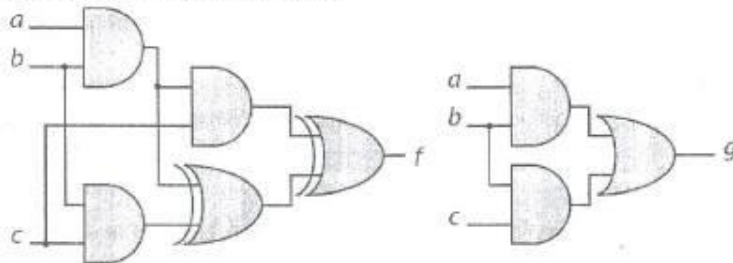
The maximum mark for the examination paper is 60 marks, and the mark obtainable for each part of a question is shown in brackets alongside the question.

Instructions to the candidates:

- ⊗ Clarify your answer with the suitable sketches as you can.
- ⊗ Please use a pen or heavy pencil to ensure legibility.
- ⊗ Please attempt all questions.

**QUESTION NUMBER ONE [25 MARKS]**

1. What are three different ways of representing a signed number? Express the decimal number (- 46) as an 8-bit numbers in each of them, and then perform each of the following operations using the 2's complement form:  
 a)  $125 + 58$ , b)  $-127 + 31$ . Determine whether there is an overflow. [7 Marks]
2. Show the bit configuration that represents the decimal number 136.6875. Hereafter, represent the decimal number 136 in gray code (You MUST show your procedure). [6 Marks]
3. Consider the two circuits shown in figure. Use Boolean algebraic transformations to prove or disprove that the two circuits given below implement the same function (do not use a truth table or Karnaugh map). [6 Marks]



4. The following is the timing diagram of a logic circuit with 3 inputs. Sketch the logic circuit that generates this waveform. Then, write the VHDL code for the circuit described by the given timing diagram (Do not simplify). [6 Marks]

